



- CPU module based on ARM9 "Dragonball" MC9328MXL
- Expansion Bus fully available
- Windows CE 5.0 ready
- Ready for Linux 2.6.13 + native component drivers
- Evaluation Board available with exhaustive development kit
- Extremely compact form factor, inexpensive connector
- Development Kit available in GNU environment
- 100% ARM7TDMI user code binary compatible
- EMI pre-compliance



PARSY is a ready-to-use CPU module based on Freescale ARM920T Dragonball microprocessor characterized by its reliability, compactness as well as low power consumption.

PARSY is suitable for palm-based solutions both in Linux and Windows CE environments. Due to 3.0V supply voltage, the intrinsic low consumption and the set of IC devoted to mobility, it is the right solution for battery supported applications.

PARSY Evaluation Board is a flexible and complete system where users can test their own applications or add their own expansion boards:

- 5V direct PSU
- Connectors for PARSY Module
- 10/100 Ethernet
- NAND + CPLD bus adaption
- JTAG interfaces
- Bootstrapping circuitry
- MMC/MS
- 3 PCI slots
- Expansion Bus connector
- RTC with back-up battery
- Touch Screen controller
- 4wire RS232 + full RS232
- Glueless interface with color TFT 240x320



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CPU	Freescale MC9328MXL "Dragonball" @ 150/200 MHz, ARM920T core v4 w/MMU Multimedia Accelerator
<u>CPU supervision</u>	Core and I/O power supply separate supervision
PSU supervisors	Core and I/O power supply separate supervision
<u>Memory</u>	
SRAM	4KBytes internal
Cache	16K cache for instructions +16K cache for data
SDRAM	32MBytes
Flash NOR	4 Mbytes
Flash NAND	0/32/128 Mbytes
EEPROM	2048 bit
<u>Interfaces (to the connector)</u>	
Ethernet PHY	1 Ready for magnetics
Ethernet MAC	1 MII 802.3u interface
UART	1 (8 Wire) + 1 (Rx/Tx)
I ² C	Master/slave/multimaster 400kHz
Timers	5
External Bus	8/16-bit byte - 29 Address Bits -5 direct Chip Select
Bus Speed	< 66MHz
PCI	host (version 2.2), arbiter supporting 3 devices
I/O Controller	yes, number tbd
Debug	JTAG IEEE 1149.1 Test Access Port + IBM TRACE
Interrupts	7 external and 19 internal interrupts
<u>Mechanical</u>	
Connectors	200 pin DDR SO-DIMM (JEDEC MO-224) <i>double height</i> format
Physical	67,50 x 50,80 x 1,00 mm ³ (2,7?x 2,0?), fixing hole
Compatibility	AMP pn 1376408, Yamaichi pn IC-657-1
PCB	8 layers
Material	FR4
Technology	double-sided SMT
Contacts	gold-plated
Temperature	0 70 °C (-40 85 °C optional) operational temperature
<u>PSU</u>	
3.3V and 1.8V	Through connector
Consumption	Less than 2,0W total power consumption
<u>Software</u>	
RTOS	eCos
Multitasking OS	Linux 2.4.20
RT module	RTAI
<u>Agency approvals</u>	
CE Mark	ITE & ISM EN 55022, EN 55011, EN 55024



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